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Search Results -

Terms	Documents
5430864.pn. and ((most adj significant adj bit) with 63)	1

Database: US Patents Full-Text Database ▼

Refine Search:
```
5430864.pn. and ((most adj significant
adj bit) with 63)
```

Search History

DB Name	Query	Hit Count	Set Name
USPT	5430864.pn. and ((most adj significant adj bit) with 63)	1	L5
USPT	5430864.pn. and (most adj significant adj bit)	1	L4
USPT	5430864.pn. and (most adj significant adj bit$)	1	L3
USPT	5430864.pn. and (least adj significant adj bit$)	1	L2
USPT	5430864.pn.	1	L1

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Search Results - Record(s) 1 through 1 of 1 returned.

☐ 1. Document ID: US 5430864 A

Entry 1 of 1   File: USPT   Jul 4, 1995

```
US-PAT-NO: 5430864
DOCUMENT-IDENTIFIER: US 5430864 A
TITLE: Extending computer architecture from 32-bits to 64-bits by using the most
significant bit of the stack pointer register to indicate word size
```

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Image |

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Terms	Documents
5430864.pn. and (most adj significant adj bit)	1

| Display 10 Documents | including document number | 1 |

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